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INFORMATION DISCLOSURE				Filing Date	January 28, 2004		
	STATEMENT BY AP			First Named Inventor	Richard E. Perego		
3	DIATEMENT DI AF		ICAN	Art Unit	Unknown		
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Sheet	1	of	4	Attorney Docket Number	RAMB-01049US2		

			U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No. <sup>1</sup>	Document Number  Number-Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		<sup>US-</sup> 6,449,679 B2*	09-10-2002	Ryan	
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Translation is attached.

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INFORMATION D	ISCLOSURE	Filing Date	January 28, 2004
STATEMENT BY		First Named Inventor	Richard E. Perego
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Sheet 2	of 4	Attorney Docket Number	RAMB-01049US2

Examiner	Cite	Document Number	U. S. PATENT Publication Date	Name of Patentee or	Pages, Columns, Lines, Where
Initials*	No. <sup>1</sup>	Number-Kind Code <sup>2 (if known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear
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				Art Unit	Unknown		
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		NON PATENT LITERATURE DOCUMENTS	
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Signature	Considered	J

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			Application Number	10/766,131		
INFO	PRMATION DI	SCLOSURE	Filing Date	January 28, 2004		
STA	TEMENT BY A	APPLICANT	First Named Inventor	Perego		
			Art Unit	2188		
	(Use as many sheets as	necessary)	Examiner Name			
Sheet	4	of 4	Attorney Docket Number	RAMB-01049US2		

NON PATENT LITERATURE DOCUMENTS						
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	<b></b>	*Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Design Considerations", February 18, 2004	***********		

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Sheet 1

### INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known				
Application Number	10/766,131			
Filing Date	Jan. 28, 2004			
First Named Inventor	Perego			
Art Unit	Unknown			
Examiner Name	Unknown			
Attorney Docket Number	RAMB-01049US2			

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#### INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known **Application Number** 10/766,131 Filing Date January 28, 2004 First Named Inventor Perego Art Unit Unknown **Examiner Name** Unknown Attorney Docket Number RAMB-01049US2

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U. S. PATENT DOCUMENTS						
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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		*JEDDELOH, JOE, "Fully Buffered DIMM (FB-DIMM)", Advanced Systems Technology, Micron Technology, Inc. April 15-16, 2004	
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<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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#### INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known Application Number 10/766,131 Filing Date January 28, 2004 First Named Inventor Perego Art Unit 2188 **Examiner Name** 

(Use as many sheets as necessary) Attorney Docket Number RAMB-01049US2 Sheet 1

	-		U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No. <sup>1</sup>	Document Number  Number-Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		<sup>US-</sup> 5883839*	03-16-1999	Tosaka et al.	
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Substitute for form 1449 FIFTADE Complete if Known Application Number 10/766,131 INFORMATION DISCLOSURE Filing Date January 28, 2004 STATEMENT BY APPLICANT First Named Inventor Perego Art Unit 2188 (Use as many sheets as necessary) Examiner Name

Attorney Docket Number Sheet RAMB-01049US2

		NON PATENT LITERATURE DOCUMENTS	
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		*IEEE Standard for Scalable Coherent Interface (SCI), "Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society", IEEE Std 1596-1992	
		*SCI, "P1596: SCI, A Scalable Coherent Interface", November 28, 1988	
		*GUSTAVSON et al., DRAFT "The Scalable Coherent Interface Project (SuperBus)", SCI, August 22, 1988, Rev. 14	
***************************************		*SCHANKE, "Proposal for Clock Distribution in SCI", SCI, May 5, 1989	
		*JAMES, "Scalable I/O Architecture for Buses", COMPCON Spring 1989, SCI, November 28, 1988	
•		*VOLZ et al., "Position Paper On Global Clock For the Futurebus +", SCI, 1989	
	•	*KRISTIANSEN et al., "Scalable Coherent Interface", to appear in Eurobus Conference Proceedings, May 1989	

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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Application Number 10/766,131

Filing Date January 28, 2004

First Named Inventor Perego

Art Unit 2188

Examiner Name

(Use as many sheets as necessary)

Sheet 1 of 2 Attorney Docket Number RAMB-01049US2

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Examiner Initials*	Cite No.1	Foreign Patent Document  Country Code <sup>3</sup> Number <sup>4</sup> 'Kind Code <sup>5</sup> ( <i>if known</i> )	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁵
		EP 0 811 916 A2*	12/10/1997	The Boeing		
•••••	***********	WO 99/30240*	06-17-1999	Intel Corporation		
***************************************		WO 99/41666*	08-19-1999	Intel Corporation		
***************************************	***************************************	WO 99/41667*	08-19-1999	Intel Corporation		
***************************************	**********	EP 1069509 A2*	01-17-2001	Texas Instruments Incorporated	***************************************	

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Substitute for form 1449B/PTO		Complete if Known
	Application Number	10/766,131
INFORMATION DISCLOSURE	Filing Date	January 28, 2004
STATEMENT BY APPLICANT	First Named Inventor	Perego
	Art Unit	2188
(Use as many sheets as necessary)	Examiner Name	
Sheet 2 of 2	Attorney Docket Number	RAMB-01049US2

		NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*					
		*PCT International Search Report (June 27, 2001) 4 pages			
	**************	*Microsoft Press Computer Dictionary, 3rd Ed., 1997, pp. 48 and 68			
		*GUSTAVSON et al., "Macintosh HD:Desktop Folder:SLDRAMrepeaterConcerns", Sunday, November 23, 1997, 4 pages			
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#### INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known **Application Number** 10/766,131 Filing Date January 28, 2004 First Named Inventor Perego Art Unit 2188 **Examiner Name** 

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Attorney Docket Number RAMB-01049US2 Sheet 1

			U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No. <sup>1</sup>	Document Number  Number-Kind Code <sup>2 (# known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		<sup>US-</sup> 5742840*	04-21-1998	Hansen et al.	
		<sup>US-</sup> 5748872*	05-05-1998	Norman	
		us-5860080*	01-12-1999	James et al.	
		<sup>US-</sup> 5867422*	02-02-1999	John	
		<sup>US-</sup> 5898863*	04-27-1999	Ofer et al.	
.,		<sup>US-</sup> 6006318*	12-21-1999	Hansen et al.	
		<sup>US-</sup> 6034878*	03-07-2000	Osaka et al.	
		us-6038682*	03-14-2000	Norman	
.,		<sup>US-</sup> 6092229*	07-18-2000	Boyle et al.	
		<sup>US-</sup> 6125419*	09-26-2000	Umemura et al.	
		<sup>US-</sup> 6151648*	11-21-2000	Haq	
	•	<sup>US-</sup> 6154826*	11-28-2000	Wulf et al.	
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		<sup>US-</sup> 6255859*	07-03-2001	Haq	
		US-6263413*	07-17-2001	Motomura et al.	
		us-6327205*	12-04-2001	Haq	
		<sup>US-</sup> 6408402*	06-18-2002	Norman	
		<sup>US-</sup> 6487102*	11-26-2002	Halbert et al.	

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		Number-Kind Code <sup>2 (# known)</sup>			Figures Appear
		<sup>US-</sup> 6502161*	12-31-2002	Perego et al.	
		<sup>US-</sup> 6513080*	01-28-2003	Haq	
		<sup>US-</sup> 6154855*	11-28-2000	Norman	
		<sup>US-</sup> 6345321*	02-05-2002	Litaize et al.	
		<sup>US-</sup> 6369605*	04-09-2002	Bonella et al.	
		<sup>US-</sup> 6449213*	09-10-2002	Dodd et a.	
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		*U.S. Patent Application Serial No. 10/713,868, filed 11-14-2003 by Pete D. Vogt						
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		*U.S. Patent Application Serial No. 10/714,026, filed 11-14-2003 by Pete D. Vogt						
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				Art Unit	2188	
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Sheet	4	of	4	Attorney Docket Number	RAMB-01049US2	

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		*JONES, F (United Memories, Inc.), "A new era of fast dynamic RAMs", IEEE Spectrum, October 1992, pp. 43-49	
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Signature	Considered		

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application Number 10/766,131

Filing Date January 28, 2004

First Named Inventor Perego

Art Unit 2188

Examiner Name

(Use as many sheets as necessary)

Substitute for form

Sheet 1 of 2 Attorney Docket Number RAMB-01049US2

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant
		Number-Kind Code <sup>2 (if known)</sup>			Figures Appear
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paterius pittiniti epite vultuuri takkitusti teese tee		us-6820163 B1	11-16-2004	McCall et al.	
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Examiner Initials*	Cite No.1	Foreign Patent Document  Country Code <sup>3</sup> Number <sup>4</sup> 'Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>			
		EP 0 282 070 B1*	08-03-1994	Fujitsu Limited					
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Examiner	Date	
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				Application Number	10/766,131	
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				Art Unit	2188	
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Sheet	2	of	2	Attorney Docket Number	RAMB-01049US2	

NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>			
		JEDEC Standard No. 21-C, Pages 4.20.4-1 through 4.20.4-69, 4.20.4- 184 PIN, PC1600/2100 DDR SDRAM REGISTERED DIMM DESIGN SPECIFICATIONM, Revision 1.2				
		MICRON, 240-Pin 256MB, 512MB, 1GB DDR2 SDRAM FBDIMM (SR, FB x72) Features, Micron Technology, Inc., 2004, pp. 1-36				
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Substitute for form 14498 Complete if Known Application Number 10/766,131 INFORMATION DISCLOSURE Filing Date January 28, 2004 First Named Inventor STATEMENT BY APPLICANT Perego Art Unit 2188 (Use as many sheets as necessary) Examiner Name Sheet Attorney Docket Number RAMB-01049US2

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Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²			
		MICRON, 256MB, 512MB, 1GB: (x72, ECC, DR) 168-Pin SDRAM RDIMM, Synchronous DRAM Module, Micron Technology, Inc., 2001, pp. 1-28				
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#### INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**Application Number** 10/766,131 January 28, 2004 Filing Date First Named Inventor Perego Art Unit 2188 **Examiner Name** 

(Use as many sheets as necessary)

Attorney Docket Number | RAMB-01049US2 Sheet |1

Examiner Initials*	Cite No. <sup>1</sup>	Document Number  Number-Kind Code <sup>2 (f known)</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		<sup>US-</sup> 6272609 B1*	08/07/2001	Jeddeloh	14 TAY - 14
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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document  Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁵			
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Complete if Known Application Number 10/766,131 Filing Date January 28, 2004 First Named Inventor Perego Art Unit 2188 Examiner Name Attorney Docket Number RAMB-01049US2

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#### **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

CONFIGURABLE WIDTH BUFFERED MODULE

Application Number:

10/766131

\*10/766131\*

Confirmation Number:

3149

First Named Applicant:

Richard Perego

Attorney Docket Number: RAMB-01049US2

Art Unit:

2188

Search string:

(5513377 or 6526469 or 6185654 or 6530033 or 5228132 ).pn.

#### **US Patent Documents**

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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	2	6526469 *	2003-02-25	Drehmel et al.			
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	4	6530033 *	2003-03-04	Raynham et al.			
	5	5228132 *	1993-07-13	Neal et al.			

#### Signature

Examiner Name	Date

#### **ELECTRONIC INFORMATION DISCLOSURE STATEMENT**

Electronic Version v18 Stylesheet Version v18.0

#### Title of Invention

#### CONFIGURABLE WIDTH BUFFERED MODULE

Application Number:

10/766131

\*10/766131\*

Confirmation Number:

3149

First Named Applicant:

Richard Perego Attorney Docket Number: RAMB-01049US2

Art Unit:

2188

Search string:

( 6330687 or 5371880 or 5475818 or 5400360 or 4631666 or 5655113 or 5511224 or 5574945 or 5917760 or 6016282 or 6449727 or 4644532 or 4667305 or 4747100 or 4864563 or 5228134 or 5355467 or 5408646 or 5537394 or 5544342 or 5553266 or 5557266 or 5604735 or 5630095 or 5659710 or 5787083 or 5802054 or 5802565 or 5805798 or 5848145 or 5884036 or 5893921 or 5900017 or 5901294 or 5911052 or

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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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### **US Published Applications**

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init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
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	2	20040260991	2004-12-23	Vogt et al.	]		
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#### Remarks

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